

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) An LCD device comprising:  
a substrate;  
a TFT having a gate electrode and source/drain electrodes on the substrate;  
a passivation film formed on an entire surface of the substrate and having a contact hole in the drain electrode of the TFT; and  
a pixel electrode made of an amorphous transparent conductive film for preventing a generation of a galvanic effect, and connected to the drain electrode through the contact hole, the drain electrode having a single-layer structure.
2. (Original) The LCD device as claimed in claim 1, wherein the pixel electrode is formed of ITO in which H<sub>2</sub>O is added.
3. (Original) The LCD device as claimed in claim 1, wherein the pixel electrode is formed of ITO in which H<sub>2</sub> is added.
4. (Original) The LCD device as claimed in claim 1, wherein the pixel electrode is formed of ITO produced at a predetermined temperature.
5. (Original) The LCD device as claimed in claim 1, wherein the pixel electrode is formed of any one of amorphous IZO and amorphous ITZO.

6. (Original) The LCD device as claimed in claim 1, wherein the pixel electrode has a thickness of approximately 500Å to 2000Å.

7. (Original) The LCD device as claimed in claim 1, wherein the pixel electrode is formed of a polycrystal transparent conductive film having a thickness of above 500Å to 2500Å.

8. (Currently Amended) A pad structure of an LCD device, comprising:

a substrate:

a metal film formed on the substrate and functioning as a pad for the LCD device; and

an amorphous transparent conductive film for preventing a generation of a galvanic effect, and formed on the metal film,  
wherein the metal film is formed of a same material as a gate line or a data line.

9. (Original) The pad structure as claimed in claim 8, wherein the amorphous transparent conductive film is formed of ITO in which H<sub>2</sub>O is added.

10. (Original) The pad structure as claimed in claim 8, wherein the amorphous transparent conductive film is formed of ITO in which H<sub>2</sub> is added.

11. (Original) The pad structure as claimed in claim 8, wherein the amorphous transparent conductive film is formed of ITO produced at a predetermined temperature.

12. (Original) The pad structure as claimed in claim 8, wherein the amorphous transparent conductive film is formed of any one of amorphous IZO and amorphous ITZO.

13. (Canceled)

14. (Previously Presented) The pad structure as claimed in claim 8, wherein the metal film has a single-layer structure.

15. (Original) The pad structure as claimed in claim 8, wherein the amorphous transparent conductive film has a thickness of approximately 500Å and 2000Å.

16. (Original) The pad structure as claimed in claim 8, wherein a polycrystal transparent conductive film having a thickness of above 500Å to 2500Å is formed instead of the amorphous transparent conductive film.

17. (Currently Amended) A method for manufacturing an LCD device, comprising the steps of:

forming a gate line including a gate electrode and a gate pad on a substrate;

forming a gate insulating film on an entire surface of the substrate;

forming a semiconductor film above the gate electrode;

forming a data line including a data pad to form source and drain electrodes of a TFT at both sides above the semiconductor film;

forming a passivation film on the entire surface of the substrate;

forming contact holes in the drain electrode, the gate pad and the data pad of the TFT; and

forming, in each pixel region, amorphous transparent conductive films for preventing a generation of a galvanic effect, and connected to the drain electrode, the gate pad and the data pad through the contact holes,

wherein at least one of the drain electrode, the gate pad and the data pad has a single-layer structure.

18. (Previously Presented) The method as claimed in claim 17, wherein at least one of the amorphous transparent conductive films is formed of ITO in which H<sub>2</sub>O is added.

19. (Original) The method as claimed in claim 17, wherein at least one of the amorphous transparent conductive films is formed of ITO in which H<sub>2</sub> is added.

20. (Original) The method as claimed in claim 17, wherein at least one of the amorphous transparent conductive films is formed of ITO produced at a predetermined temperature.

21. (Original) The method as claimed in claim 17, wherein at least one of the amorphous transparent conductive films is formed of any one of amorphous IZO and amorphous ITZO.

22. (Original) The method as claimed in claim 17, further comprising the step of:

performing a thermal process to at least one of the amorphous transparent conductive films at a temperature of around 150° to 350°.

23. (Original) The method as claimed in claim 17, wherein at least one of the amorphous transparent conductive films is formed at a thickness of approximately 500Å to 2000Å.

24. (Original) The method as claimed in claim 17, wherein a polycrystal transparent conductive film having a thickness of above 500Å and 2500Å is formed instead of at least one of the amorphous transparent conductive films.

25. (Canceled)